

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

ATTORNEY DOCKET NUMBER: 200312685-1

IN RE APPLICATION OF: U.S. Patent No. 7,065,697

USPTO CONFIRMATION CODE: 1123

APPLICATION NO.: 10/632,207

FILED: Jul. 29, 2003

EXAMINER: Stephen M. Baker

GROUP ART UNIT: 2133

FOR: SYSTEMS AND METHODS OF PARTITIONING DATA TO FACILITATE
ERROR CORRECTION

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number 10/632,207, now Patent Number 7,065,697.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to Deposit Account No. 082025.

All errors were the fault of the applicant and, accordingly, please charge **\$100.00** to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The errors were the fault of both the applicant and USPTO and, accordingly, please charge **\$100.00** to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application are as follows:

In column 4, lines 42-46, after "unit 12." delete "Each adjacent bit pair domain includes a data portion and a correction bit portion. The number of bits in an adjacent bit pair domain corresponds to the number of bits correctable by the error corrector 13 in the error correction unit 12."

(Amendment to specification filed on Dec. 02, 2005, page 6, line 18)

In column 10, line 58, in Claim 6, after "plurality" delete ",".

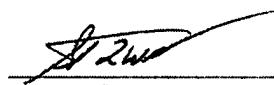
(Claims filed on Jul. 29, 2003, page 15, in Claim 6, line 2)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

1/23/09

DATE


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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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PATENT NO. : 7,065,697

APPLICATION NO. : 10/632,207

ISSUE DATE : Jun. 20, 2006

INVENTOR(S) : Theodore Carter Briggs, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, lines 42-46, after "unit 12." delete "Each adjacent bit pair domain includes a data portion and a correction bit portion. The number of bits in an adjacent bit pair domain corresponds to the number of bits correctable by the error corrector 13 in the error correction unit 12.".

In column 10, line 58, in Claim 6, after "plurality" delete ",".

MAILING ADDRESS OF SENDER (Please do not use customer number below):

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

This collection of information is required by 37 CFR 1.322, and 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patent, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.